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APR 2 7 2006 Serial No. Form PTO 1449 Atty. Docket No. U.S. Department of Commerce Patent and Francisch Office 10/651,596 (Rev. 2-32) CISCO-7506 Applicant: Daniel C. Biederman Information Disclosure Statement by Applicant Group: 2116 Filed: 8/29/2003 (Use several sheets if necessary) **U.S. Patent Documents** Class Subclass Filing Date Name Document No. Date Init. **Foreign Documents** Translation Subclass Yes No Class Document No. Date Country Init. Other Documents (Including Author, Title, Date, Pertinent Pages, etc.) Kiss, Peter, "Chapter 3: Cascaded Delta-Sigma ADCs", paper: Adaptive Digital Compensation of MJB Analog Circuit Imperfections for Cascaded Delta-Sigma Analog-to-Digital Converters," Center for Design of Analog-Digital Integrated Circuits, Oregon State Univ., Dec. 31, 1999, pp.45-58. Date Considered Examiner 06 Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.